

Discription

The BTRD03A5 is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time ,make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

Applications

- | Cellular phones audio
- | MP3 players
- | Digital cameras
- | Portable applications
- | mobile telephone

Features

- | Small Body Outline Dimensions: 0.61 mm x 0.31 mm
- | Low Body Height: 0.28 mm
- | Low Leakage
- | Response Time is Typically < 1 ns
- | ESD Rating of Class 3 (> 16 kV) per Human Body Model
- | IEC61000-4-2 Level 4 ESD Protection
- | These are Pb-Free Devices
- | We declare that the material of product compliance with RoHS requirements.



DFN0603-D

Ordering information

Device	Shipping
BTRD03A5	15000/Tape&Reel

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Air discharge Contact discharge		±25 ±20	kV kV
ESD Voltage Per Human Body Model		16	kV
Total Power Dissipation on FR-5 Board (Note 1) @ T _A =25°C	PD	200	mW
Junction and Storage Temperature Range	T _J ,T _{STG}	-55 to 150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	°C

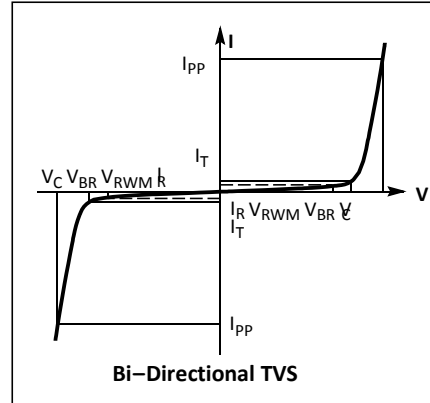
Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-5 = 1.0*0.75*0.62 in.

ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
I _T	Test Current
P _{pk}	Peak Power Dissipation
C	Capacitance @ V _R = 0 and f = 1.0 MHz



ELECTRICAL CHARACTERISTICS

Device	V _{RWM} (V)	I _{R1} (μA) @ V _{RWM}	I _{R2} (μA) @ V _R =3.5V	V _{BR} (V) @ I _T (Note 2)		I _T	V _C (V) @ I _{PP} = 1 A (Note 3)	V _C (V) @ MAX I _{PP} (Note 3)	I _{PP} (A) (Note 3)	P _{PK} (W) (Note 3)	C (pF)
	Max	Max	Max	Min	Max	mA	Max	Max	Max	Max	Max
BTRD03A5	5.0	0.5	0.3	5.6	9.5	1.0	9.8	12.5	5.5	69	15

Other voltage available upon request.

- V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C
- Surge current waveform per Figure 3.

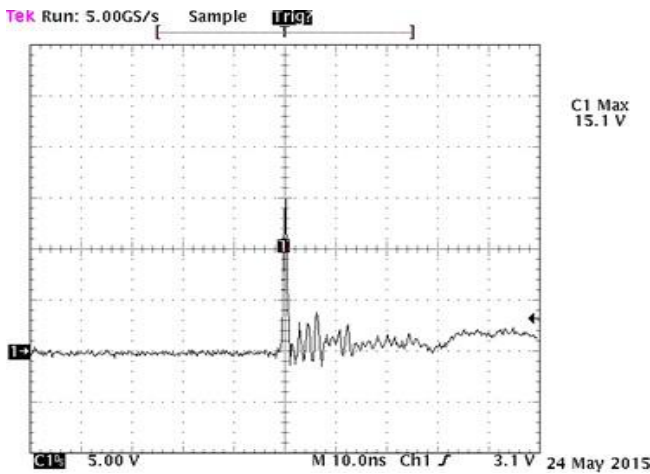


Fig1. ESD Clamping Voltage Screenshot Positive 8 kV Contact per IEC61000-4-2

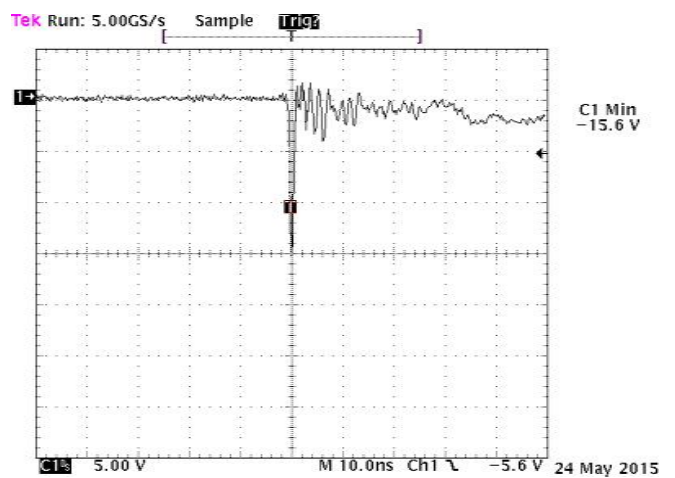


Fig2. ESD Clamping Voltage Screenshot Negative 8 kV Contact per IEC61000-4-2

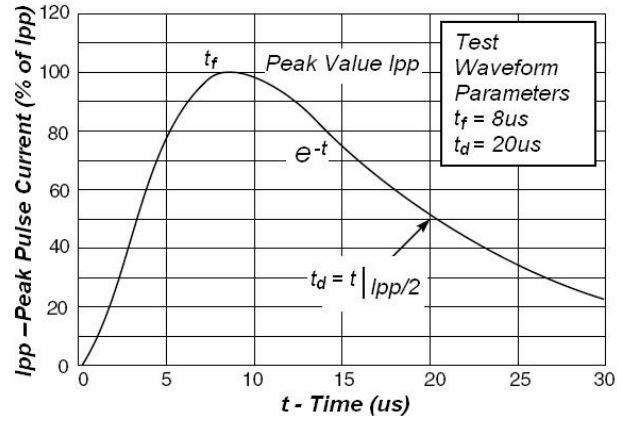
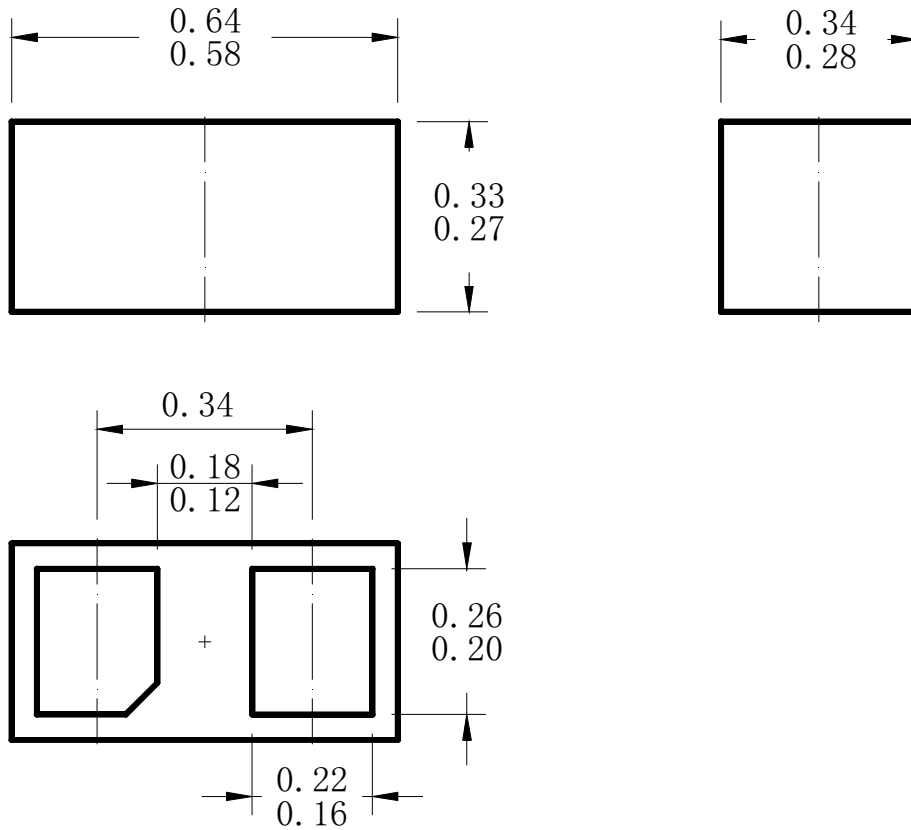


Fig3. Pulse Waveform

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DIMENSION OUTLINE:

Unit:mm



Soldering Footprint

